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ART UNIT: 2823

SERIAL NO.: 09/784,424

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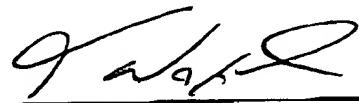
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REGISTRATION NUMBER: 48,027

TECHNOLOGY CENTER 2800

**PHILIPS ELECTRONICS NORTH AMERICA CORPORATION
580 WHITE PLAINS ROAD
TARRYTOWN, NEW YORK 10591
TELEPHONE: (914) 333-9608**

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

YOURI PONOMAREV

NL 000139

Serial No.: 09/784,424

Art Unit: 2823

Filed: February 15, 2001

Examiner: M. ESTRADA

Title: SEMICONDUCTOR DEVICE WITH AN INTEGRATED CMOS CIRCUIT
WITH MOS TRANSISTORS HAVING SILICON-GERMANIUM ($Si_{1-x}Ge_x$)
GATE ELECTRODES, AND METHOD OF MANUFACTURING SAME

*15/1
9/25/02
JWW*

Assistant Commissioner for Patents
Washington, D.C. 20231

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AMENDMENT

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Sir:

In response to the official Office Action dated August 28, 2002
Applicant submits the following amendments and remarks.

IN THE CLAIMS

Please cancel Claims 5-8 without prejudice.

REMARKS

This application has been carefully reviewed in light of
the Office Action dated August 28, 2002. In response to the
Office Action's requirement for restriction, Applicant elects
group I, claims 1-4 drawn to a semiconductor device, without
traverse.